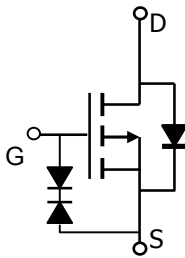
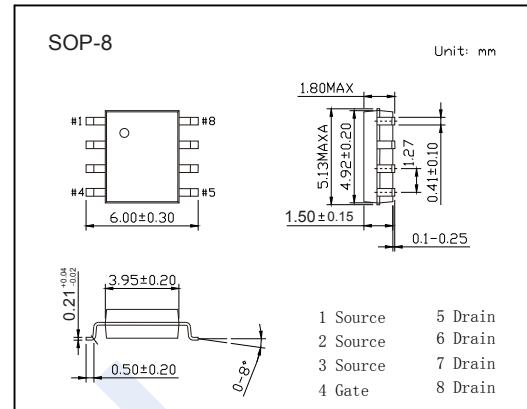


P-Channel MOSFET

AO4425 (KO4425)

■ Features

- $V_{DS} (V) = -38V$
- $I_D = -14 A (V_{GS} = -20V)$
- $R_{DS(ON)} < 10m\Omega (V_{GS} = -20V)$
- $R_{DS(ON)} < 11m\Omega (V_{GS} = -10V)$
- ESD Rating: 3000V HBM



■ Absolute Maximum Ratings $T_a = 25^\circ C$

| Parameter | Symbol | Rating | Unit | |
|---|------------|--------------------|------------|--------------|
| Drain-Source Voltage | V_{DS} | -38 | V | |
| Gate-Source Voltage | V_{GS} | ± 25 | | |
| Continuous Drain Current | I_D | $T_A = 25^\circ C$ | -14 | A |
| | | $T_A = 70^\circ C$ | -11 | |
| Pulsed Drain Current | I_{DM} | -50 | | |
| Power Dissipation | P_D | $T_A = 25^\circ C$ | 3.1 | W |
| | | $T_A = 70^\circ C$ | 2 | |
| Thermal Resistance.Junction- to-Ambient | R_{thJA} | $t \leq 10s$ | 40 | $^\circ C/W$ |
| | | Steady-State | 75 | |
| Thermal Resistance.Junction- to-Lead | R_{thJL} | 24 | | |
| Junction Temperature | T_J | 150 | $^\circ C$ | |
| Junction Storage Temperature Range | T_{stg} | -55 to 150 | | |

P-Channel MOSFET

AO4425 (KO4425)

■ Electrical Characteristics Ta = 25°C

| Parameter | Symbol | Test Conditions | Min | Typ | Max | Unit |
|---------------------------------------|---------------------|--|-----|------|------|------|
| Drain-Source Breakdown Voltage | V _{DSS} | I _D =-250 μA, V _{GS} =0V | -38 | | | V |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} =-30V, V _{GS} =0V | | | -100 | nA |
| | | V _{DS} =-30V, V _{GS} =0V, T _J =55°C | | | -500 | |
| Gate-Body leakage current | I _{GSS} | V _{DS} =0V, V _{GS} =±20V | | | ±1 | μA |
| | | V _{DS} =0V, V _{GS} =±25V | | | ±10 | |
| Gate Threshold Voltage | V _{GS(th)} | V _{DS} =V _{GS} , I _D =-250 μA | -2 | | -3.5 | V |
| Static Drain-Source On-Resistance | R _{DS(on)} | V _{GS} =-20V, I _D =-14A | | | 10 | mΩ |
| | | V _{GS} =-20V, I _D =-14A, T _J =125°C | | | 13.5 | |
| | | V _{GS} =-10V, I _D =-14A | | | 11 | |
| On state drain current | I _{D(ON)} | V _{GS} =-10V, V _{DS} =-5V | -50 | | | A |
| Forward Transconductance | g _{FS} | V _{DS} =-5V, I _D =-14A | | 43 | | S |
| Input Capacitance | C _{iss} | V _{GS} =0V, V _{DS} =-20V, f=1MHz | | 3800 | | pF |
| Output Capacitance | C _{oss} | | | 560 | | |
| Reverse Transfer Capacitance | C _{rss} | | | 350 | | |
| Gate resistance | R _g | V _{GS} =0V, V _{DS} =0V, f=1MHz | | 7.5 | | Ω |
| Total Gate Charge | Q _g | V _{GS} =-10V, V _{DS} =-20V, I _D =-14A | | 63 | | nC |
| Gate Source Charge | Q _{gs} | | | 14.1 | | |
| Gate Drain Charge | Q _{gd} | | | 16.1 | | |
| Turn-On DelayTime | t _{d(on)} | V _{GS} =-10V, V _{DS} =-20V, R _L =1.35Ω, R _{GEN} =3Ω | | 12.4 | | ns |
| Turn-On Rise Time | t _r | | | 9.2 | | |
| Turn-Off DelayTime | t _{d(off)} | | | 97.5 | | |
| Turn-Off Fall Time | t _f | | | 45.5 | | |
| Body Diode Reverse Recovery Time | t _{rr} | I _F =-14A, dI/dt=100A/us | | 35 | | nC |
| Body Diode Reverse Recovery Charge | Q _{rr} | | | 33 | | |
| Maximum Body-Diode Continuous Current | I _S | | | | -4.2 | A |
| Diode Forward Voltage | V _{SD} | I _S =-1A, V _{GS} =0V | | | -1 | V |

Note : The static characteristics in Figures 1 to 6 are obtained using <300μs pulses, duty cycle 0.5% max.

■ Marking

| | |
|---------|----------------|
| Marking | 4425 KC**** |
|---------|----------------|

P-Channel MOSFET AO4425 (KO4425)

■ Typical Characteristics

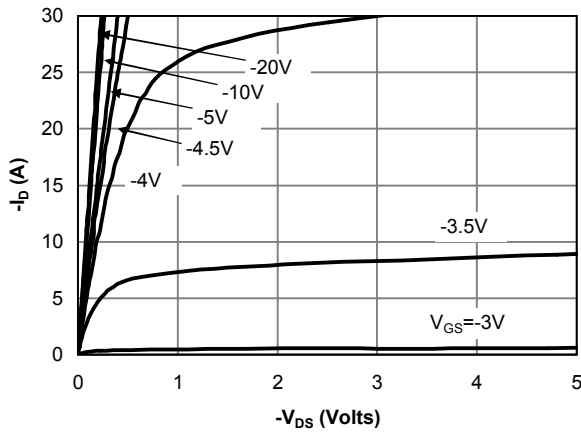


Figure 1: On-Region Characteristics

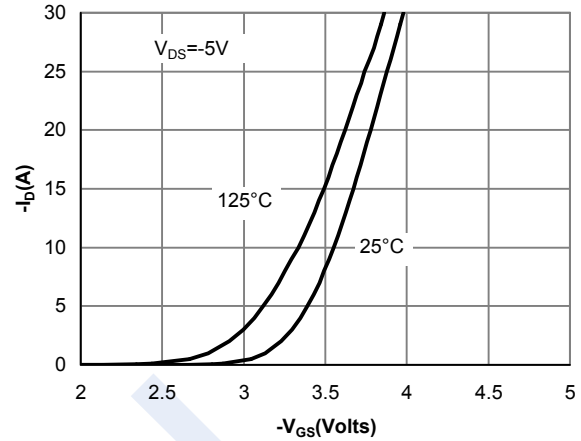


Figure 2: Transfer Characteristics

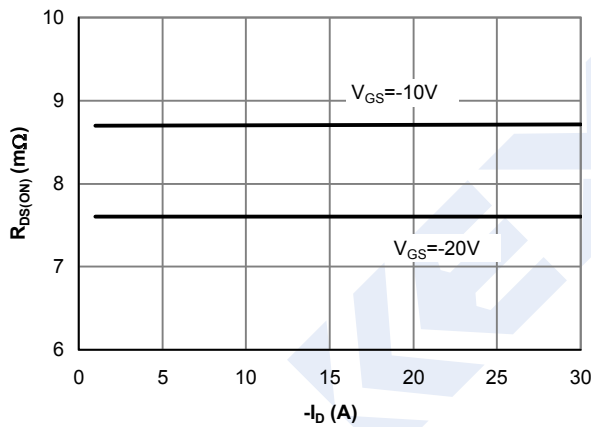


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

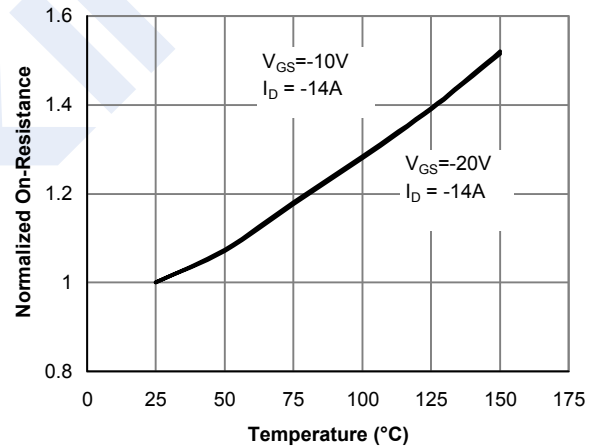


Figure 4: On-Resistance vs. Junction Temperature

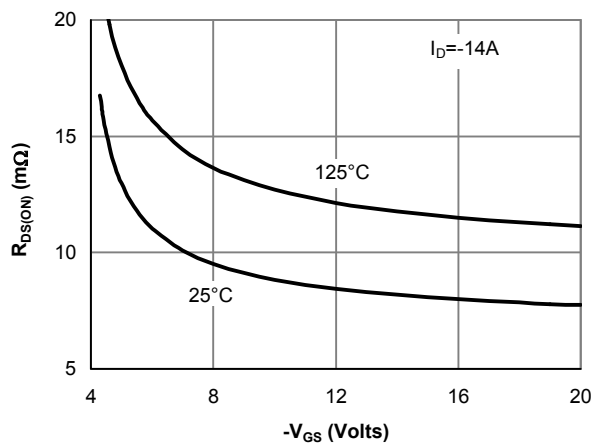


Figure 5: On-Resistance vs. Gate-Source Voltage

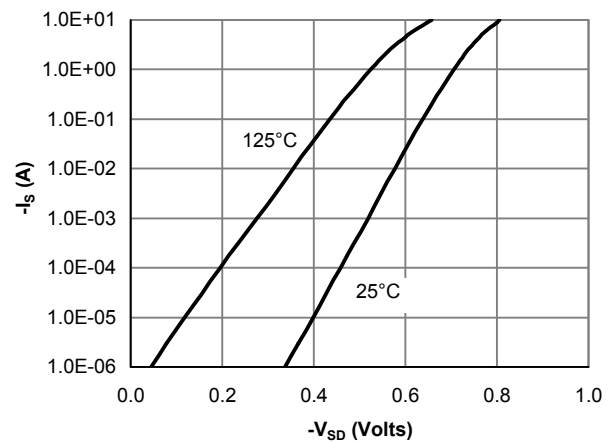


Figure 6: Body-Diode Characteristics

P-Channel MOSFET AO4425 (KO4425)

■ Typical Characteristics

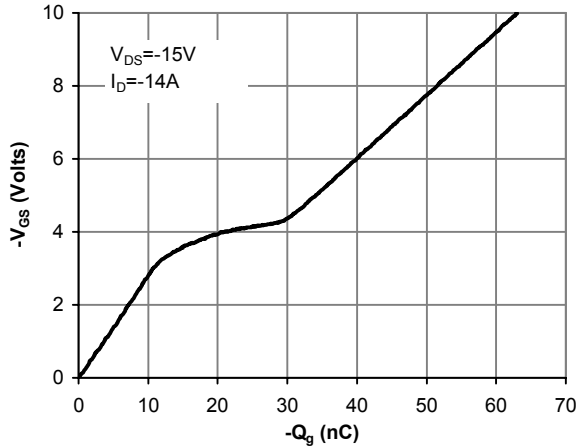


Figure 7: Gate-Charge Characteristics

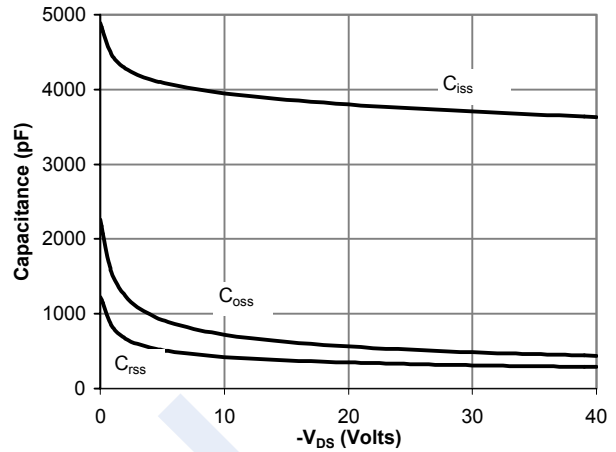


Figure 8: Capacitance Characteristics

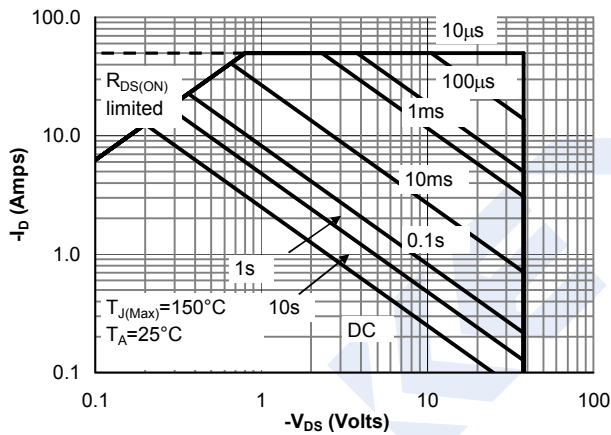


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

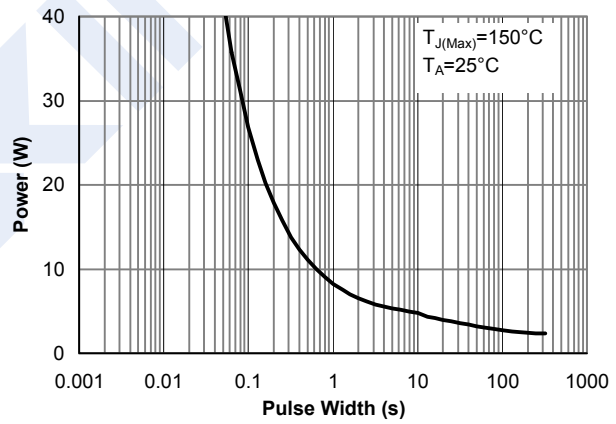


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

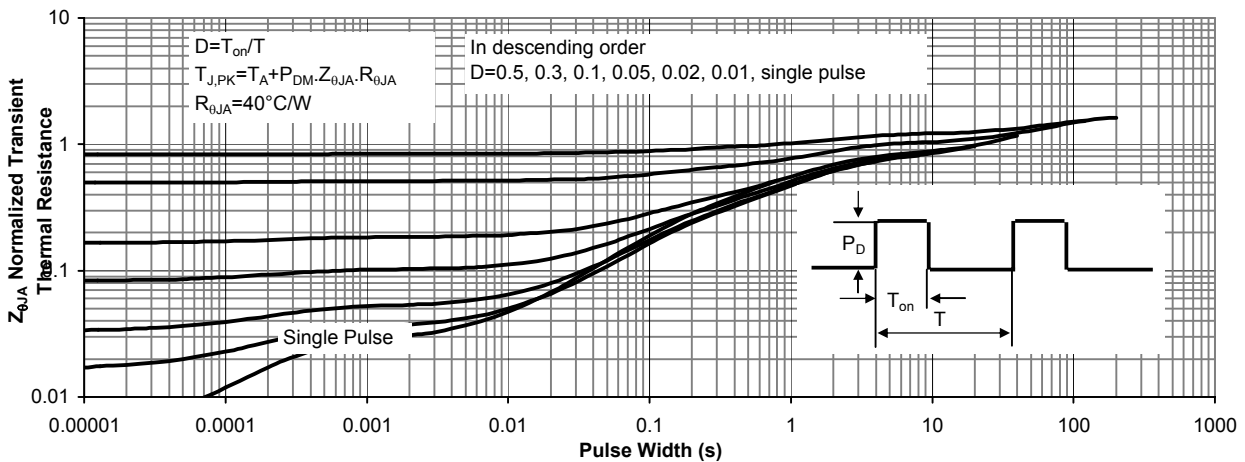


Figure 11: Normalized Maximum Transient Thermal Impedance